

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Applicant:	Shawn L. Lloyd et al.	§	Art Unit:	2833
Serial No.:	10/822,572	§	Examiner:	James R. Harvey
Filed:	April 12, 2004	§	Docket:	ITL.1113US P18785
For:	Making Electrical Connections Between a Circuit Board and an Integrated Circuit	§	Assignee:	Intel Corporation

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Commissioner for Patents  
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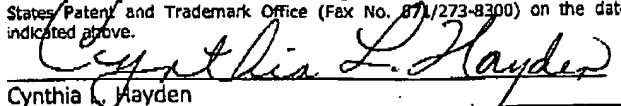
**SUPPLEMENTAL REPLY TO FINAL REJECTION**

Sir:

As a supplemental reply to the final rejection, please amend the above-referenced patent application as follows:

Date of Deposit: April 3, 2007

I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 877/273-8300) on the date indicated above.

  
Cynthia L. Hayden

TROP, PRUNER &amp; HU, P.C.

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<b>Company:</b>	US Patent Office	<b>Date:</b>	April 3, 2007
<b>Fax:</b>	(571) 273-8300	<b>Pages:</b>	7 (including cover sheet)
<b>Your Re:</b>	10/822,572	<b>Our Re:</b>	ITL.1113US

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